

ABSTRACT OF THE DISCLOSURE

An apparatus for improving the detection of IDE bus cable configuration is provided. The apparatus refers to a type of latching device having a triggering terminal and an output terminal that links up a detection device and an IDE bus. The triggering terminal couples with a signal lead of the IDE bus. The output terminal couples with a signal detection terminal of the detection device so that the output terminal of the latching device is reset to a low potential in a system reset. The output terminal of the latching device outputs a high potential when the triggering terminal of the latching device receives any signal variation. The IDE bus is diagnosed as having an 80-pin cable configuration when a low potential is sent from the output terminal of the D-type flip-flop to the signal detection terminal of the GPIO controller. Conversely, the IDE bus is diagnosed as having a 40-pin cable configuration when a high potential is sent from the output terminal of the D-type flip-flop to the signal detection terminal of the GPIO controller.

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